



For ESD protection - C series

Part No.	Working Voltage (Vw)	Clamping Voltage (Vc)	ESD Withstanding	Capacitance (C)		Capacitance Tolerance
	Volts	Volts	Times	pF		%
	<15 μ A	1A,8/20 μ s	8KV*	1KHz	1MHz	
0402						
JMV0402C050T4R7	5.0	50.0	> 1000	-	4.7	-20%~+80%
JMV0402C050T100	5.0	50.0	> 1000	-	10	\pm 20%
JMV0402C050T120	5.0	50.0	> 1000	-	12	\pm 20%
JMV0402C050T150	5.0	50.0	> 1000	-	15	\pm 20%
JMV0402C050T180	5.0	50.0	> 1000	-	18	\pm 20%
JMV0402C050T220	5.0	50.0	> 1000	-	22	\pm 20%
JMV0402C050T270	5.0	50.0	> 1000	-	27	\pm 20%
JMV0402C050T330	5.0	50.0	> 1000	-	33	\pm 20%
JMV0402C050T390	5.0	50.0	> 1000	-	39	\pm 20%
JMV0402C050T470	5.0	50.0	> 1000	-	47	\pm 20%
JMV0402C050T560	5.0	50.0	> 1000	-	56	\pm 20%
JMV0402C050T680	5.0	50.0	> 1000	-	68	\pm 20%
JMV0402C050T820	5.0	50.0	> 1000	-	82	\pm 20%
JMV0402C050T101	5.0	30.0	> 1000	100	-	\pm 20%
JMV0402C050T121	5.0	30.0	> 1000	120	-	\pm 20%
JMV0402C050T151	5.0	29.0	> 1000	150	-	\pm 20%
JMV0402C050T181	5.0	29.0	> 1000	180	-	\pm 20%
JMV0402C050T221	5.0	27.0	> 1000	220	-	\pm 20%
JMV0402C050T271	5.0	27.0	> 1000	270	-	\pm 20%
JMV0402C050T331	5.0	26.0	> 1000	330	-	\pm 20%
JMV0402C120T4R7	12.0	80.0	> 1000	-	4.7	-20%~+80%
JMV0402C120T100	12.0	60.0	> 1000	-	10	\pm 20%
JMV0402C120T120	12.0	60.0	> 1000	-	12	\pm 20%
JMV0402C120T220	12.0	50.0	> 1000	-	22	\pm 20%
JMV0402C120T330	12.0	50.0	> 1000	-	33	\pm 20%
JMV0402C120T470	12.0	50.0	> 1000	-	47	\pm 20%
JMV0402C120T560	12.0	50.0	> 1000	-	56	\pm 20%
JMV0402C120T820	12.0	50.0	> 1000	-	82	\pm 20%
JMV0402C120T101	12.0	50.0	> 1000	100	-	\pm 20%
JMV0402C240T3R3	24.0	200.0	> 1000	-	3.3	-20%~+80%
JMV0402C240T4R7	24.0	130.0	> 1000	-	4.7	-20%~+80%

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Part No.	Working Voltage (Vw)	Clamping Voltage (Vc)	ESD Withstanding	Capacitance (C)		Capacitance Tolerance
	Volts	Volts	Times	pF		%
	<15 μ A	1A,8/20 μ s	8KV*	1KHz	1MHz	
0603						
JMV0603C050T4R7	5.0	50.0	> 1000	-	4.7	-20% ~ +80%
JMV0603C050T100	5.0	50.0	> 1000	-	10	\pm 20%
JMV0603C050T120	5.0	50.0	> 1000	-	12	\pm 20%
JMV0603C050T150	5.0	50.0	> 1000	-	15	\pm 20%
JMV0603C050T180	5.0	50.0	> 1000	-	18	\pm 20%
JMV0603C050T220	5.0	50.0	> 1000	-	22	\pm 20%
JMV0603C050T270	5.0	50.0	> 1000	-	27	\pm 20%
JMV0603C050T330	5.0	50.0	> 1000	-	33	\pm 20%
JMV0603C050T390	5.0	50.0	> 1000	-	39	\pm 20%
JMV0603C050T470	5.0	50.0	> 1000	-	47	\pm 20%
JMV0603C050T560	5.0	50.0	> 1000	-	56	\pm 20%
JMV0603C050T680	5.0	50.0	> 1000	-	68	\pm 20%
JMV0603C050T820	5.0	50.0	> 1000	-	82	\pm 20%
JMV0603C050T101	5.0	30.0	> 1000	100	-	\pm 20%
JMV0603C050T151	5.0	29.0	> 1000	150	-	\pm 20%
JMV0603C050T181	5.0	29.0	> 1000	180	-	\pm 20%
JMV0603C050T221	5.0	27.0	> 1000	220	-	\pm 20%
JMV0603C050T271	5.0	27.0	> 1000	270	-	\pm 20%
JMV0603C050T331	5.0	26.0	> 1000	330	-	\pm 20%
JMV0603C050T391	5.0	26.0	> 1000	390	-	\pm 20%
JMV0603C050T471	5.0	26.0	> 1000	470	-	\pm 20%
JMV0603C050T102	5.0	23.0	> 1000	1000	-	\pm 20%
JMV0603C120T4R7	12.0	80.0	> 1000	-	4.7	-20% ~ +80%
JMV0603C120T100	12.0	60.0	> 1000	-	10	\pm 20%
JMV0603C120T150	12.0	50.0	> 1000	-	15	\pm 20%
JMV0603C120T220	12.0	50.0	> 1000	-	22	\pm 20%
JMV0603C120T330	12.0	50.0	> 1000	-	33	\pm 20%
JMV0603C120T390	12.0	50.0	> 1000	-	39	\pm 20%
JMV0603C120T470	12.0	50.0	> 1000	-	47	\pm 20%
JMV0603C120T560	12.0	50.0	> 1000	-	56	\pm 20%
JMV0603C120T820	12.0	50.0	> 1000	-	82	\pm 20%
JMV0603C120T101	12.0	50.0	> 1000	100	-	\pm 20%
JMV0603C120T151	12.0	50.0	> 1000	150	-	\pm 20%
JMV0603C120T181	12.0	47.0	> 1000	180	-	\pm 20%
JMV0603C120T331	12.0	46.0	> 1000	330	-	\pm 20%
JMV0603C240T3R3	24.0	200.0	> 1000	-	3.3	-20% ~ +80%

* - In system ESD withstanding pulse per IEC 61000-4-2 , 8KV , contact discharge method.

Vw - The max. steady state DC operating voltage of which varistor could maintain also not exceeding 15 μ A leakage current.

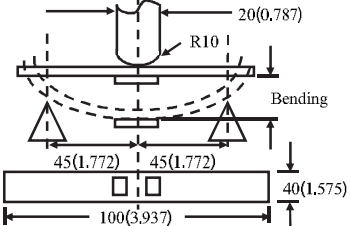
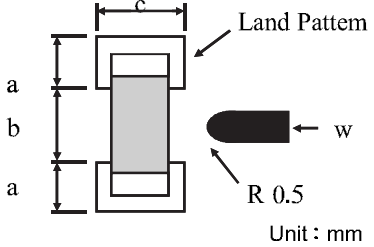
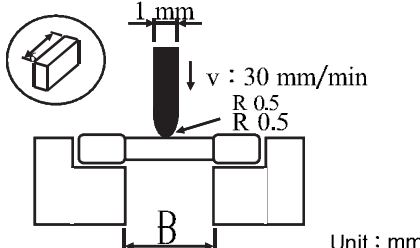
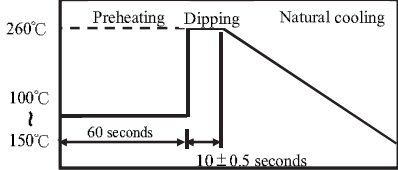
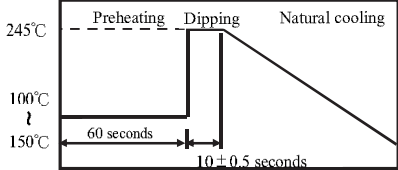
Vc - The peak voltage acrossed the varistor measured at a specified pulse current and waveform.

C - The device capacitance measured with 1.0 Vrms and 1KHz / 0.5 Vrms and 1MHz.

*Any special design or request is welcomed. Please contact our e-mail address : sales@joyin.com.tw

JOYIN CO., LTD.
Metal Oxide Varistor

■ Reliability-Multilayer Chip Varistor

Test description	Standard	Performance	Test condition																																								
Board flexure strength	IEC60068-2-21	No mechanical damage shall be noticed even when the board is bent 2mm (0.079inches)	Solder a chip on a test substrate. Bend the substrat by 2mm(0.079in) 																																								
Flexure strength	Specification Standard	The terminal electrode and chip body must not be damaged by the forces applied. <table border="1" data-bbox="438 929 1005 1086"> <thead> <tr> <th>SIZE</th> <th>0402</th> <th>0603</th> <th>0805</th> <th>1206</th> <th>1210</th> <th>1812</th> <th>2220</th> </tr> </thead> <tbody> <tr> <td>a</td> <td>-</td> <td>1.0</td> <td>1.0</td> <td>1.3</td> <td>1.3</td> <td>1.5</td> <td>1.8</td> </tr> <tr> <td>b</td> <td>-</td> <td>0.8</td> <td>1.0</td> <td>1.5</td> <td>1.5</td> <td>3.6</td> <td>4.6</td> </tr> <tr> <td>c</td> <td>-</td> <td>1.3</td> <td>1.3</td> <td>3.0</td> <td>3.0</td> <td>3.8</td> <td>5.8</td> </tr> <tr> <td>w(kgf)</td> <td>-</td> <td>1.0</td> <td>4.0</td> <td>5.0</td> <td>5.0</td> <td>5.0</td> <td>5.0</td> </tr> </tbody> </table>	SIZE	0402	0603	0805	1206	1210	1812	2220	a	-	1.0	1.0	1.3	1.3	1.5	1.8	b	-	0.8	1.0	1.5	1.5	3.6	4.6	c	-	1.3	1.3	3.0	3.0	3.8	5.8	w(kgf)	-	1.0	4.0	5.0	5.0	5.0	5.0	
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c	-	1.3	1.3	3.0	3.0	3.8	5.8																																				
w(kgf)	-	1.0	4.0	5.0	5.0	5.0	5.0																																				
Bending strength	IEC60068-2-21	The ceramic chip shall not be damaged be the forces applied under the following conditions. <table border="1" data-bbox="438 1220 1005 1332"> <thead> <tr> <th>TYPE</th> <th>0402</th> <th>0603</th> <th>0805</th> <th>1206</th> <th>1210</th> <th>1812</th> <th>2220</th> </tr> </thead> <tbody> <tr> <td>D(mm)</td> <td>-</td> <td>1.3</td> <td>1.3</td> <td>2.0</td> <td>2.0</td> <td>3.8</td> <td>4.8</td> </tr> <tr> <td>W(kgf)</td> <td>-</td> <td>2.0</td> <td>3.0</td> <td>4.0</td> <td>4.0</td> <td>5.0</td> <td>5.0</td> </tr> </tbody> </table>	TYPE	0402	0603	0805	1206	1210	1812	2220	D(mm)	-	1.3	1.3	2.0	2.0	3.8	4.8	W(kgf)	-	2.0	3.0	4.0	4.0	5.0	5.0																	
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W(kgf)	-	2.0	3.0	4.0	4.0	5.0	5.0																																				
Resistance to solder heat	IEC60068-2-20	The ceramic chip shall not be damaged. Shall be covered with solder. Vb: Within ±10% of the initial value.	Preheat:100°C~150°C,60seconds Solder temperature:260±5°C Dip time:10±1 seconds 																																								
Solderability	IEC60068-2-58	More than 90% of terminal electrode shall be covered with solder.	Preheat:100°C~150°C,60seconds Solder temperature:245±3°C Dip time:3±0.3seconds 																																								

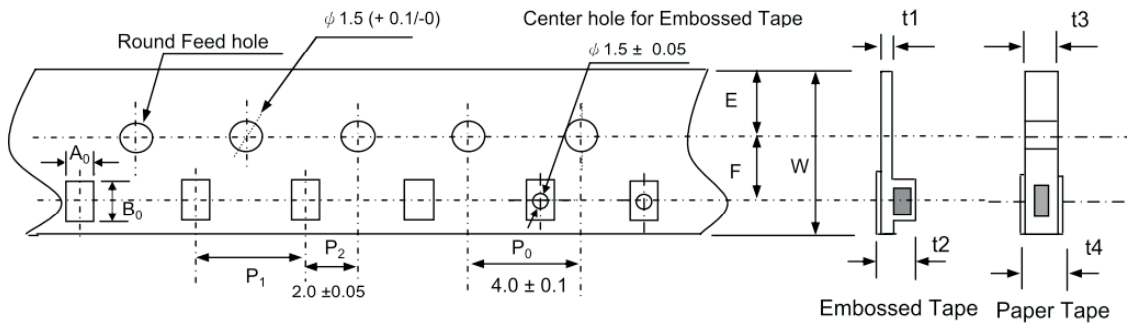
防靜電保護

JOYIN CO., LTD.
Metal Oxide Varistor

■ Reliability-Multilayer Chip Varistor

Test description	Standard	Performance	Test condition
High temperature Load	IEC61051-1	Appearance:ceramic chip shall not be damaged. Vb:Within±10% of the initial value	Temperature: 85±2℃ Testing time:1000±24hours Load Voltage:Working voltage Measurement : After placing for 24 hours min.
Damp Heat Load, Steady State	IEC60068-2-78	Appearance:ceramic chip shall not be damaged. Vb:Within±10% of the initial value	Humidity:90 to 95% RH Temperature: 40±2℃ Testing time:500±24 hours at V _{DC} Measurement : After placing for 24 hours min.
Rapid Change of Temperature	IEC61051-1	Appearance:Cracking, chipping or any other defects harmful to the characteristics shall not be allowed Vb:Within±10% of the initial value	Temperature: -40,+125℃.Keeping 30 minutes Cycle:100 cycles Measurement : After placing for 24 hours min.
Low temperature storage	IEC61051-1	Appearance:Cracking, chipping or any other defects harmful to the characteristics shall not be allowed Vb:Within±10% of the initial value	Temperature: -40±5℃ Testing time:1000±24hours Measurement:After placing for 24 hours min.
High temperature storage	IEC61051-1	Appearance:Cracking, chipping or any other defects harmful to the characteristics shall not be allowed Vb:Within±10% of the initial value	Temperature:125±5℃ Testing time:1000±24hours Measurement:After placing for 24 hours min.
Max. Energy	Specification Standard	Appearance:ceramic chip shall not be damaged. Vb:Within±10% of the initial value	10/1000usWaveform, Wmax, 1 surge current
ESD test	IEC61000-4-2	Appearance:ceramic chip shall not be damaged. Vb:Within ±50% of the initial value (For MLV-P/C/E/JES application test only)	Discharge:Air discharge Voltage:15kV Polarity:+, - Number:10 times in 10 seconds. Discharge: Contact discharge Voltage:8kV Polarity:+, - Number:10 times in 10 seconds.

Carrier Tape Specifications



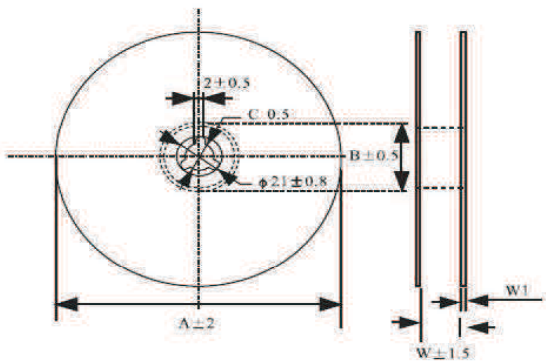
Dimensions of Embossed Tape

Size	A ₀ ±0.1 (mm)	B ₀ ±0.1 (mm)	P ₁ ±0.1 (mm)	t ₁ /t ₂ (mm)	t ₃ /t ₄ (mm)	Quantity/Reel(Pcs)	
						Paper Tape	Embossed Tape
0402	0.62	1.10	2	—	1.0max/ 1.1max	10000	—
0603	1.08	1.88	4	—	1.0max/ 1.1max	4000	—
0805	1.42	2.30	4	0.6max/2.0max	1.0max/ 1.1max	4000	4000
1206	1.88	3.50	4	0.6max/2.9max	—	—	3000
1210	2.18	3.46	4	0.6max/2.9max	—	—	2000
1812	3.66	4.95	8	0.6max/2.9max	—	—	1000
2220	5.10	5.97	8	0.6max/2.9max	—	—	1000

A₀ : Width of Cavity
 B₀ : Length of Cavity
 P₁ : Pitch

t₁ : Embossed Tape Thickness
 t₂ : Height of Embossed Tape
 t₃ : Paper Tape for Width
 t₄ : Paper Tape Bottom Width

Reel Specifications



Dimensions

Size	A	B	C	W	1W
0402	178	60	13	10	1.6
0603	178	60	13	10	1.6
0805	178	60	13	10	1.6
1206	178	60	13	10	1.6
1210	178	60	13	10	1.6
1812	178	60	13.5	13.6	1.6
2220	178	60	13.5	13.6	1.6